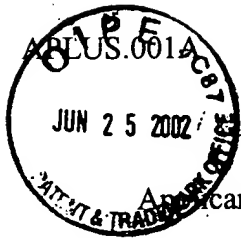


#3  
09/11/02



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Songjie Xu ) Group Art Unit 2123  
App. No. : 09/754,406 )  
Filed : January 2, 2001 )  
For : METHODOLOGY AND )  
APPLICATIONS OF TIMING- )  
DRIVEN LOGIC RESYNTHESIS FOR )  
VLSI CIRCUITS (as amended) )  
Examiner : Unknown )

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INFORMATION DISCLOSURE STATEMENT

United States Patent and Trademark Office  
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Arlington, VA 22202

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. This Information Disclosure Statement is being filed before the receipt of a first Office Action on the merits, and presumably no fee is required in accordance with 37 C.F.R. § 1.97(b)(3). If a first Office Action on the merits was mailed before the mailing date of this Statement, the Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: June 21, 2002

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